

Method And Apparatus For High Speed IC Test Interface
Dalal et al.
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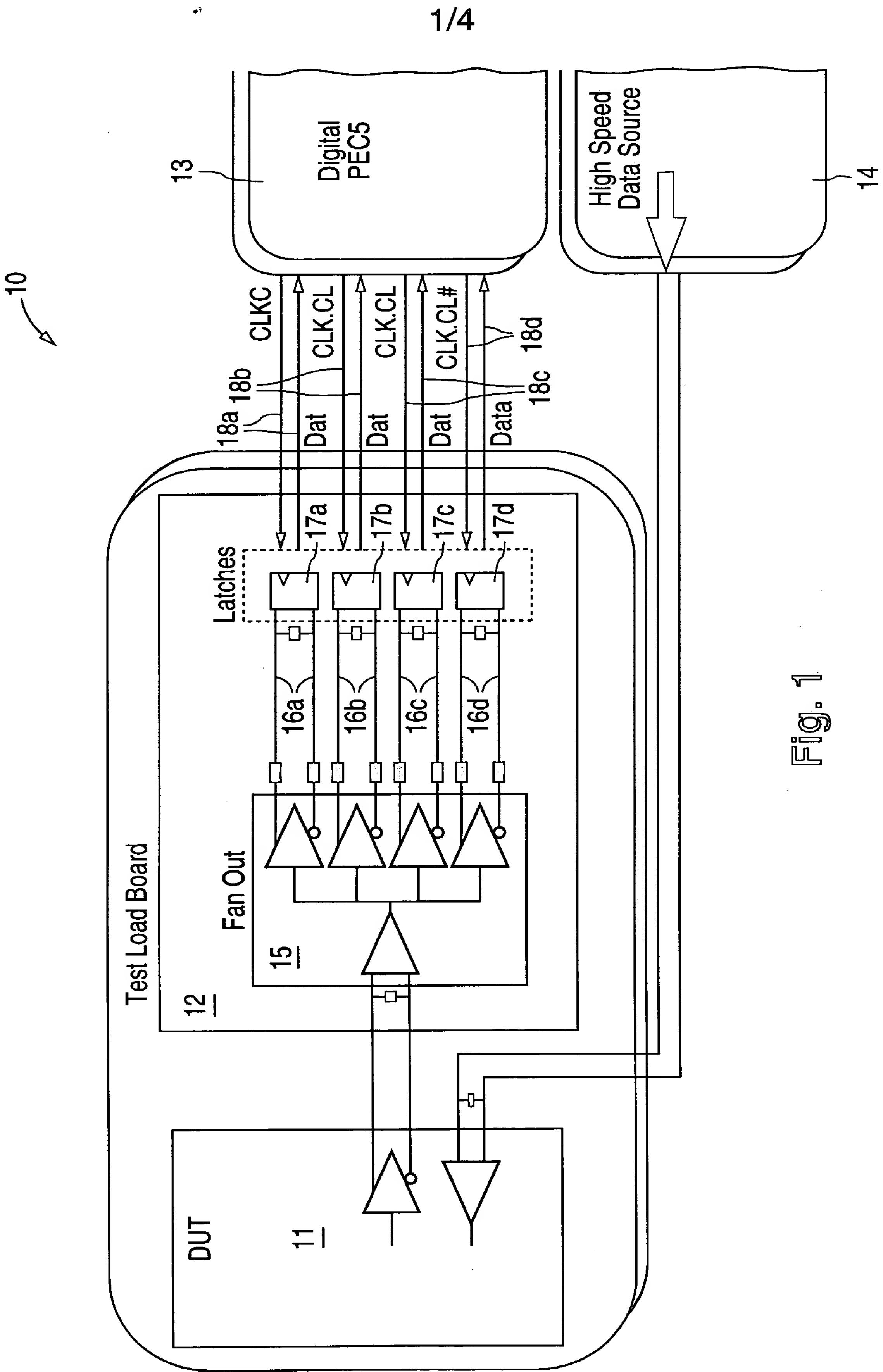


Fig. 1

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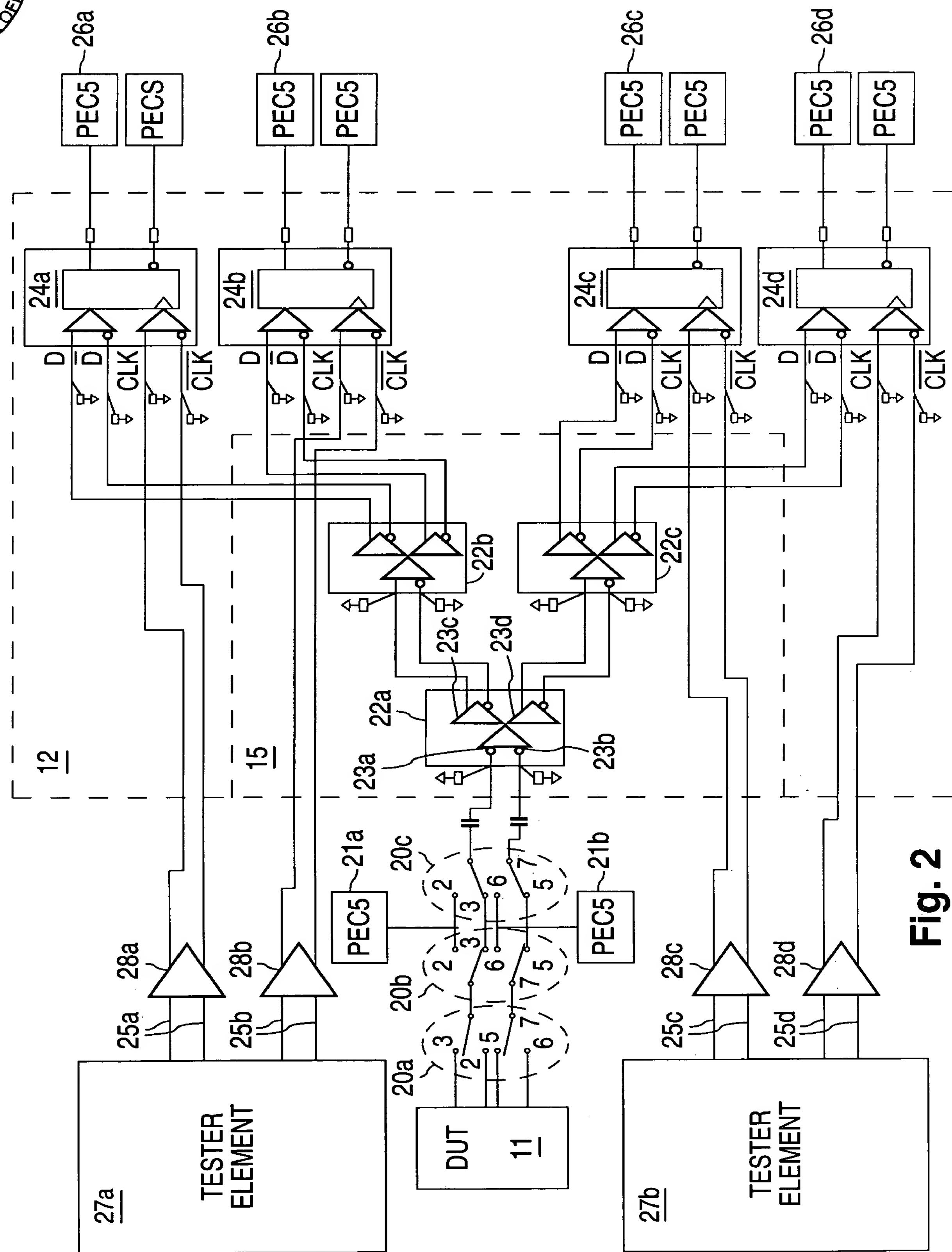


Fig. 2

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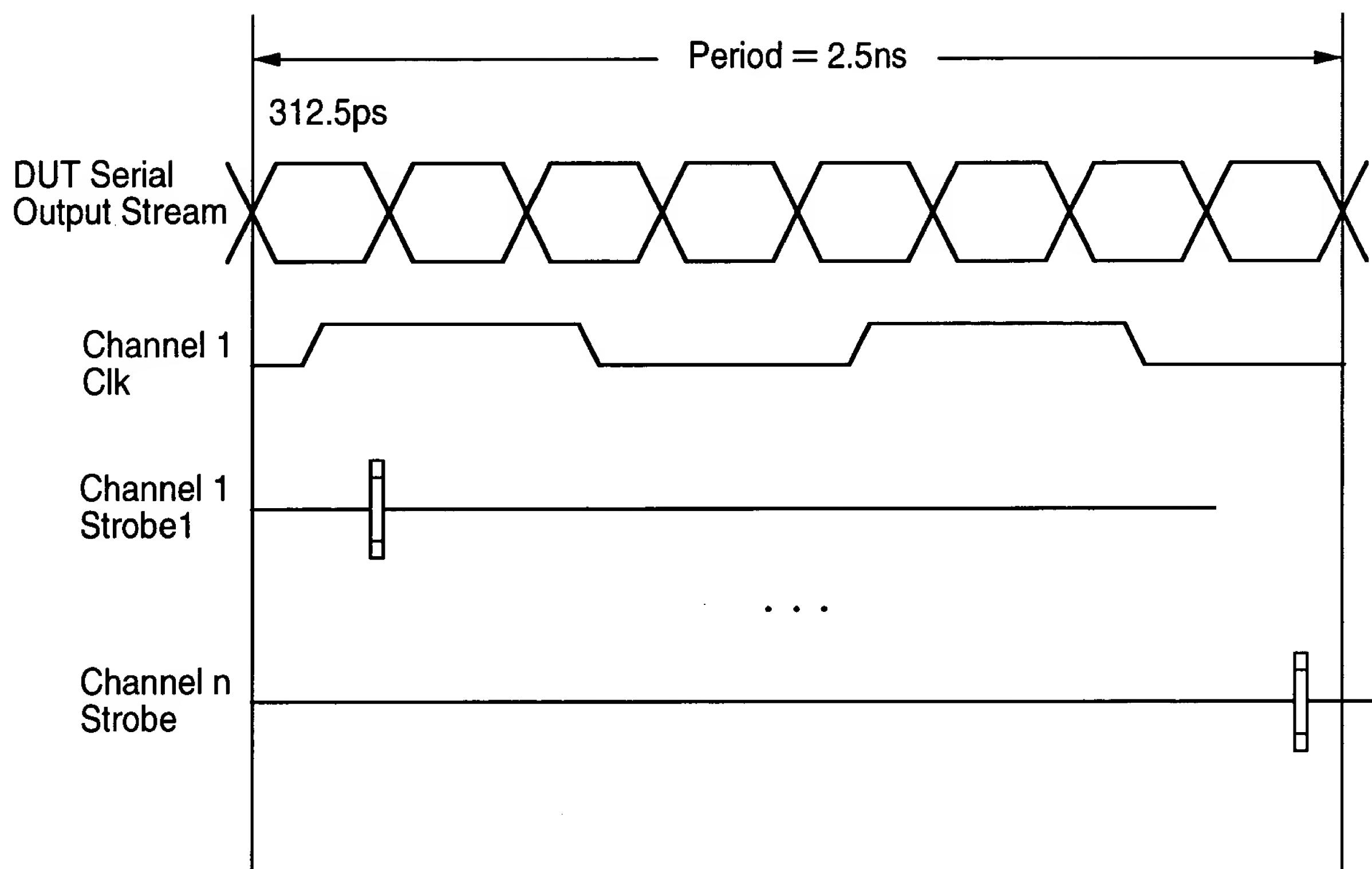


Fig. 3

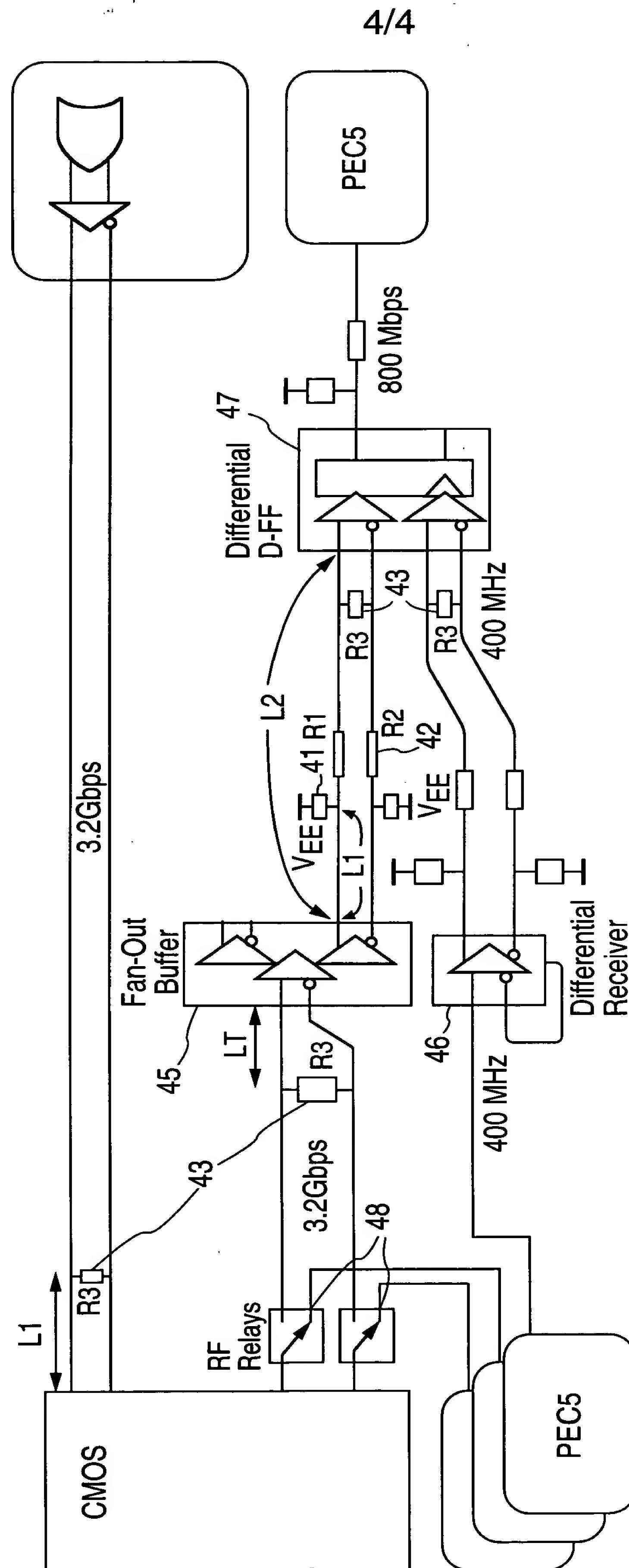
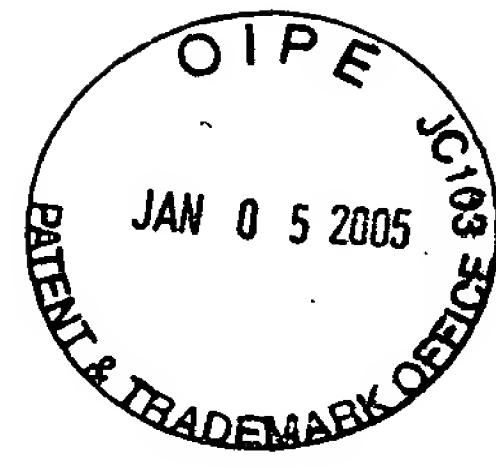


Fig. 4